



UNITED STATES PATENT AND TRADEMARK OFFICE

MAILED FROM  
DIRECTORS OFFICE

JUL 20 2006

TECHNOLOGY CENTER 2800

Commissioner for Patents  
United States Patent and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450  
www.uspto.gov

Paper No. 071706

HOGAN & HARTSON LLP  
ONE TABOR CENTER, SUITE 1500  
1200 SEVENTEENTH ST  
DENVER CO 80202

*In re* Application of

Kim C. Hardee

Application No.: 10776101

Filed: Sept. 19, 2005

For: **HIGH SPEED POWER-GATING TECHNIQUE FOR  
INTEGRATED CIRCUIT DEVICES INCORPORATING A  
SLEEP MODE OF OPERATION**

:  
:  
: **WITHDRAWAL FROM ISSUE**  
: *37 CFR 1.313*  
:

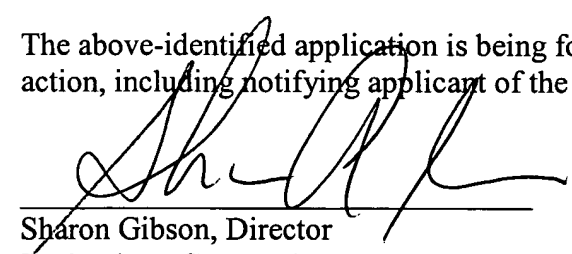
The purpose of this communication is to inform you that the above-identified application is being withdrawn from issue pursuant to 37 CFR 1.313.

The application is being withdrawn from issue because of unpatentability of at least one claim.

The US Patent and Trademark Office records reveal that the issue fee has not been paid. If the issue fee has been submitted, the applicant may request a refund, or may request that the fee be credited to a deposit account. However, applicant may wait until the application is either again found allowable or held abandoned. If the application is allowed, upon receipt of a new Notice of Allowance and Fee(s) Due, applicant may request that the previously submitted issue fee be applied toward payment of the issue fee in the amount identified on the new Notice of Allowance and Fee(s) Due. If the application is abandoned, applicant may request either a refund or a credit to a Deposit Account.

Any question regarding this communication should be directed to Amir Zarabian, Supervisory Patent Examiner, at (571) 272-1852.

The above-identified application is being forwarded to the examiner for prompt appropriate action, including notifying applicant of the new status of this application.

  
Sharon Gibson, Director  
Technology Center 2800  
Semiconductors, Electrical and Optical  
Systems and Components